

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Lee A. Burton

Serial No. 10/618,041

Filed: July 11, 2003

For: SWITCH/NETWORK ADAPTER PORT
INCORPORATING SHARED MEMORY
RESOURCES SELECTIVELY ACCESSIBLE BY A
DIRECT EXECUTION LOGIC ELEMENT AND ONE
OR MORE DENSE LOGIC DEVICES

Art Unit: 2182

Examiner: Sorrell, Eron J.

Confirmation No.: 4115

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97

MAIL STOP ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.97(i), please place the attached Form PTO/SB/08A of the listed patent references in the above-referenced file. In submitting these references, no representation is made or implied that the references are or are not material to the examination of this application. A copy of the non-patent references are included.

A Notice of Allowance was mailed in this case on May 1, 2008. The Issue Fee has not yet been paid.

This Information Disclosure Statement is filed with no request for consideration of these references. Accordingly, no fee is believed due. However, any fee associated herewith may be charged to Deposit Account No. 50-1123.

30 July 2008
Date

Respectfully submitted,


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